



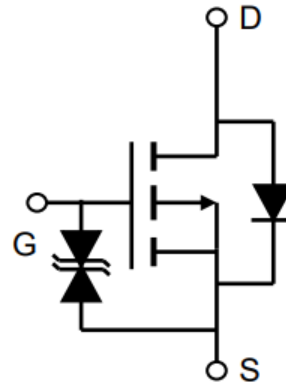
SSC8021GN1

P-Channel MOSFET with ESD Protection

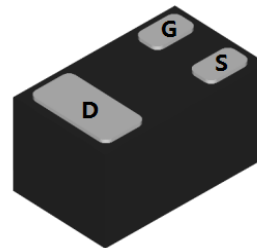
➤ Features

VDS	VGS	RDSON Typ.	ID	ESD
-20V	±12V	0.45R@-4V5	-1A	0.5kV
		0.6R@-2V5		

➤ Pin configuration



Bottom view



➤ Description

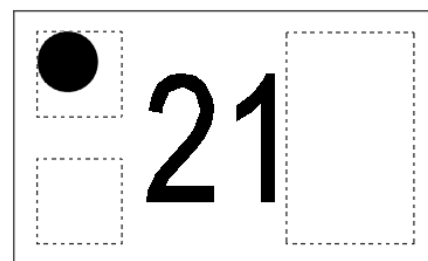
This device particularly suits low voltage applications such as portable equipment, power management and other battery powered circuits, and low in-line power dissipation are needed in a very small outline surface mount package. The product does not contain Rohs substances such as lead and halogen.

➤ Applications

- Load Switch
- Portable Devices
- Signal Drive

➤ Ordering Information

Device	Package	Shipping
SSC8021GN1	DFN1006-3L	10K/Reel



Marking



➤ **Absolute Maximum Ratings**($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
V_{DSS}	Drain-to-Source Voltage	-20	V
V_{GSS}	Gate-to-Source Voltage	± 12	V
I_D	Continuous Drain Current ^a	-1	A
I_{DM}	Pulsed Drain Current ^b	-3	A
P_D	Power Dissipation ^c	0.45	W
P_{DSM}	Power Dissipation ^a	0.2	W
T_J	Operation junction temperature	-55 to 150	$^{\circ}\text{C}$
T_{STG}	Storage temperature range	-55 to 150	$^{\circ}\text{C}$

➤ **Thermal Resistance Ratings**($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Maximum	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance ^a	625	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}$	Junction-to-Case Thermal Resistance	277	

Note:

- The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz.copper,in a still air environment with $T_A=25^{\circ}\text{C}$.The value in any given application depends on the user is specific board design. The current rating is based on the $t \leq 10\text{s}$ thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The power dissipation P_D is based on $T_{J(MAX)}=150^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.

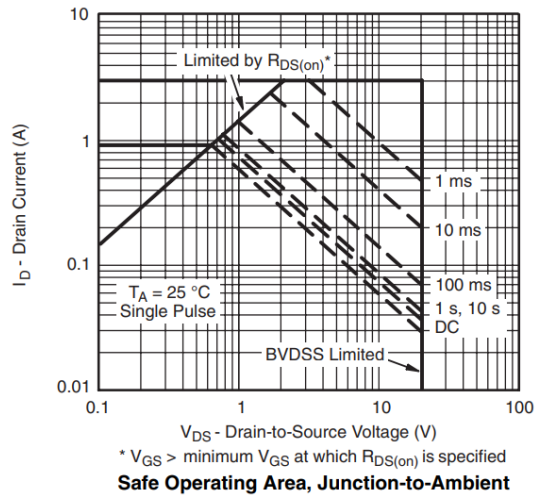
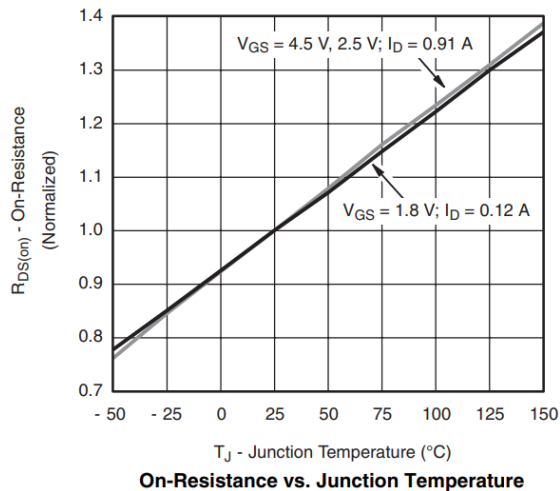
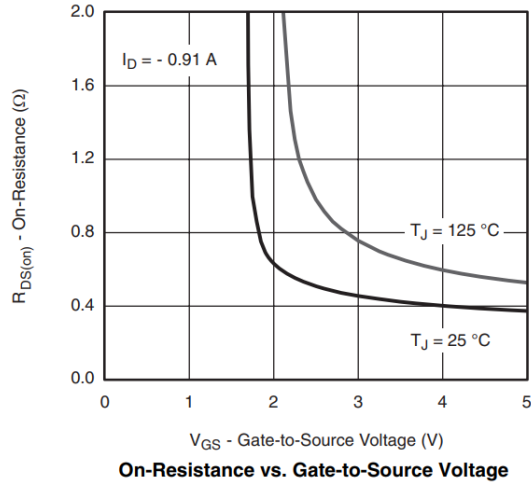
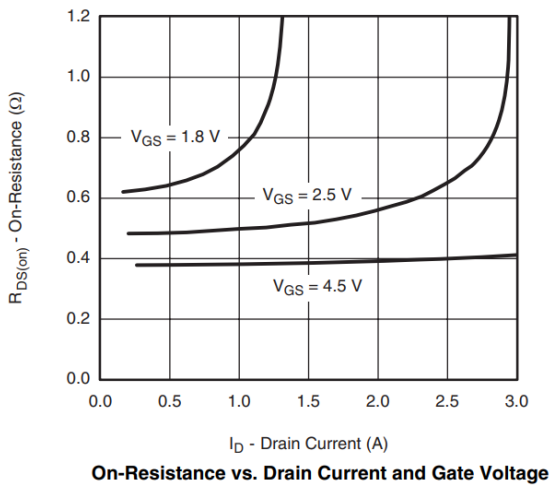
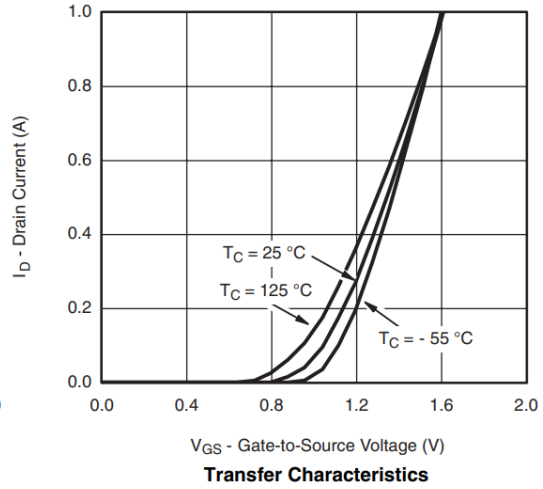
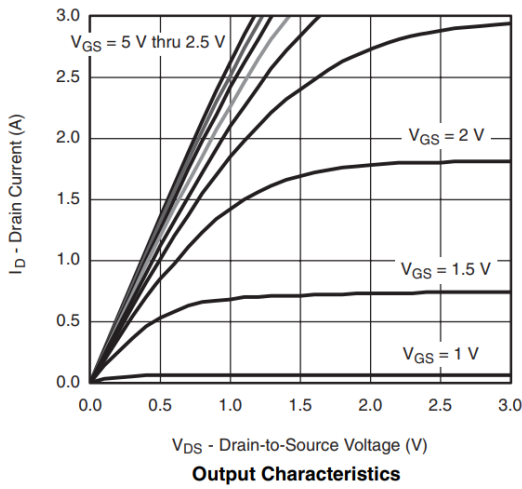


➤ **Electronics Characteristics**($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Unit
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-20			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.4	-0.7	-1	V
$R_{DS(on)}$	Drain-Source On- Resistance	$V_{GS}=-4.5V, I_D=-0.5A$		450	650	mR
		$V_{GS}=-2.5V, I_D=-0.3A$		600	900	
		$V_{GS}=-1.8V, I_D=-0.1A$		800	1500	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=-16V, V_{GS}=0V$			-1	μA
I_{GSS}	Gate-Source leak current	$V_{GS}=\pm 10V, V_{DS}=0V$			± 10	μA
G_{FS}	Transconductance	$V_{DS}=-5V, I_D=-0.5A$		1		S
V_{SD}	Forward Voltage	$V_{GS}=0V, I_S=-0.15A$			-1.3	V
C_{iss}	Input Capacitance	$V_{DS}=-10V, V_{GS}=0V, f=1MHz$		25		pF
C_{oss}	Output Capacitance			12		
C_{rss}	Reverse			5		
	Capacitance					
$T_{D(ON)}$	Turn-on delay time	$V_{GS}=-4.5V,$ $V_{DS}=-10V, R_L=20R, R_G=3R$		5		ns
T_r	Rise time			4		
$T_{D(OFF)}$	Turn-off delay time			12		
T_f	Fall time			7		

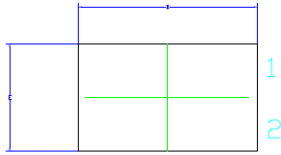


➤ **Typical Characteristics** ($T_A=25^\circ\text{C}$ unless otherwise noted)

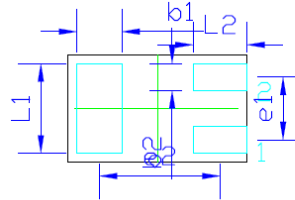




➤ Package Information



TOP VIEW



BOTTOM VIEW



SIDE VIEW

COMMON DIMENSION (MM)			
PKG	DFN1006		
REF.	MIN.	NOM.	MAX.
A	0.45	0.50	0.55
D	0.95	1.00	1.05
E	0.55	0.60	0.65
k1	0.20	0.25	0.30
k2	0.10	0.15	0.20
L1	0.45	0.50	0.55
L2	0.25	0.30	0.35
e1	0.350 BSC		
e2	0.675 BSC		

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